DC Parameters

National Semiconductor Application Note 1873 Debbie Lai June 19, 2008



Vos (Input Offset Voltage)

In an ideal case, in any op-amp circuit, when 0V is applied to the input, 0V should be expected at the output. In actuality, there is a voltage present at the output. This voltage is due to the number of mismatches of the op-amp's internal transistors and resistance. The value of Vos is determined by the voltage required to make the output voltage 0V.

TABLE 1. Offset Voltage for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25$ °C, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10$ k Ω to $V^+/2$. Boldface limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
			(Note 6)	(Note 5)	(Note 6)	
Vos	Input Offset Voltage	V _{CM} = 4.5V		±6	±50	
	(Note 7)				±120	
		V _{CM} = 0.5V		±6	±40	μV
					±100	

When looking at a datasheet, it is important to see at what voltage and conditions the part is specified at. The part in Figure 1, the LMP7731, is specified at 5V, at 25 °C, V+ = 5V, $V^- = 0V$, $V_{CM} = V^+/2$, $R_1 > 10k\Omega$ to $V^+/2$. The first column is the abbreviated symbol of the parameter. The second column is the name and description of the parameter. In addition, there might be some added conditions to a particular parameter located in the third column. The offset voltage to the LMP7731 is specified at $V_{CM} = 4.5V$ and 0.5V instead of the usual 2.5V. There are three test conditions, a Minimum, Typical and Maximum values. These are guaranteed values for the performance of this part. Also, for this parameter there are bolded maximum values, which indicate the condition at temperature extremes. The last column states the unit of the parameter.

In the "Typical Characteristics" section of this datasheet, there are various curves displaying different parameters. These curves are used to help understand the specified values of the datasheet.

In Figure 2, the offset voltage is -6µV at 5V and 25°C.

Offset Voltage vs. Supply Voltage

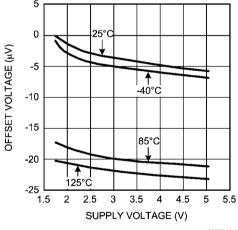


FIGURE 1. Offset Voltage vs. $V_{\rm CM}$ for LMP7731 (Typical Characteristics)

TCV_{OS} (Input Offset Voltage Drift)

 ${\sf TCV}_{\sf os}$ is defined as the temperature coefficient of the offset voltage. The value of ${\sf TCV}_{\sf os}$ is determined by the amount of change in input offset voltage per degree Celsius.

TABLE 2. Offset Voltage Drift for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
TCV _{OS}	Input Offset Voltage Drift	V _{CM} = 4.5V		±0.5	±1.0	
		V _{CM} = 0.5V		±0.2	±0.8	μV/°C

Like the offset voltage, the offset voltage drift has the conditions of $V_{CM} = 4.5 V$ and 0.5V instead of the usual 2.5V.

In the upper left corner of Figure 4, the parameters of the graph are shown, with the supply voltage at 3.3 and 5V, common mode voltage at 0.5V and the temperature range from -40 $^{\circ}$ C to 125 $^{\circ}$ C.

The TCV $_{OS}$ typical value for the LMP7731 according to Figure 4 is -0.2 μ V/°C.

TCV_{OS} Distribution

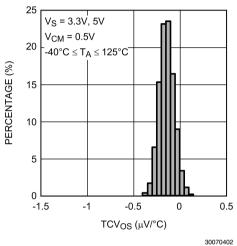


FIGURE 2. Percentage vs. TCV_{os} Distribution for LMP7731 (Typical Characteristics)

I_{IN} (Input Current)

input terminals. The input current is defined as the sum of the currents that flow into the op-amp.

An ideal op-amp has no current flowing in the input terminals. In actuality, there are small currents that flow through both

TABLE 3. Input Current for LMP2011 (Electrical Characteristics)

5V DC Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_J = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = 2.5V$ and $R_L > 1M\Omega$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 3)	Typ (Note 2)	Max (Note 3)	Units
I _{IN}	Input Current			-3		pА

Looking at the top of the datasheet, Figure 5, the LMP2011 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, V_O = 2.5V, R_L

>1 $M\Omega$. There are no additional conditions associated with this parameter.

I_{OS} (Input Offset Current)

The difference between the currents flowing into the input terminals.

TABLE 4. Input Offset Current for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
los	Input Offset Current	V _{CM} = 4.5V		±1	±50 ±70	nΛ
		V _{CM} = 0.5V		±11	±65 ±80	nA

Looking at the top of the datasheet, Figure 6, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, $V_{CM} = V+/2$, R_L

>10k Ω to V+/2. This particular parameter has the conditions of V $_{CM}=4.5V$ and V $_{CM}=0.5V.$

TCI_{OS} (Input Offset Current Drift)

The change in input offset current due to the change in temperature.

TABLE 5. Input Offset Current Drift for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
TCI _{OS}	Input Offset Current Drift	$V_{CM} = 0.5V$ and $V_{CM} = 4.5V$		0.0482		nA/°C

Looking at the top of the datasheet, Figure 7, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, $V_{CM} = V^{+}/2$, $P_{CM} = V^{+}/2$, $P_{CM} = V^{+}/2$. This particular particular of $V_{CM} = 0.5$ V and $V_{CM} = 4.5$ V.

>10k Ω to V+/2. This particular parameter has the conditions

I_B (Bias Current)

current that flows into both terminals. The bias current is calculated as the average of the two input currents.

In an ideal op-amp circuit, there should be no current flowing in or out of the input terminals. But in actuality, there is a small

TABLE 6. Bias Current for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
I _B	Input Bias Current	V _{CM} = 4.5V		±1.5	±30 ±50	- A
		V _{CM} = 0.5V		±14	±50 ±85	nA

Looking at the top of the datasheet, Figure 7, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, V_{CM} = V+/2, R_L >10k Ω to V+/2. This particular parameter has the conditions of $V_{CM}=0.5V$ and $V_{CM}=4.5V$.

In the upper right corner of Figure 8, the conditions of this graph are shown with the supply voltage at 5V.

The input bias current value at $V_{CM} = 0.5V$ at 25°C is at 14nA The input bias current value at $V_{CM} = 4.5V$ at 25°C is at 1.5nA

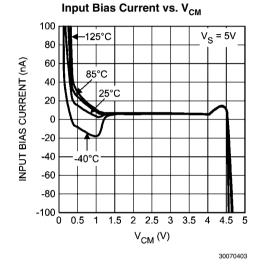


FIGURE 3. Input Bias Current vs. VCM For LMP7731 (Typical Characteristics)

CMRR (Common Mode Rejection Ratio)

age. CMRR is defined as $20^*log(\Delta V_{CM}\!/\!\Delta V_{OS})$, in decibels

This parameter is calculated as the change in input offset voltage with respect to the change in the common mode volt-

TABLE 7. Common Mode Rejection Ratio for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min	Тур	Max	Units
			(Note 6)	(Note 5)	(Note 6)	
CMRR	Common Mode Rejection Ratio	$0.15V \le V_{CM} \le 0.7V$	101	120		
		$0.23 \le V_{CM} \le 0.7V$	89			٩D
		1.5V ≤ V _{CM} ≤ 4.85V	105	130		dB
		1.5V ≤ V _{CM} ≤ 4.77V	99			

Looking at the top of the datasheet, Figure 10, the LMP7731 is specified at 5V, at 25 °C, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_1 > 10 k\Omega$ to V+/2. This particular parameter has the V_{CM} conditions ranging between 0.15 and 0.7, 0.23 and 0.7, 1.5 and 4.85, and 1.5 and 4.77 volts.

In the upper right corner of Figure 11, the conditions of this parameter are given as the supply voltage at 2.5, 3.3, and 5

The CMRR for the LMP7731 at 5V is measured to be 130dB.

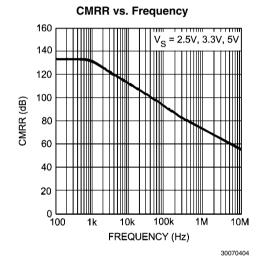


FIGURE 4. Common Mode Rejection Ratio vs. Frequency For LMP7731 (Typical Characteristics)

PSRR (Power Supply Rejection Ratio)

This parameter is calculated as the change in input offset voltage with respect to the change in power supply voltage. PSRR is defined as $20*\log(\Delta V_{OS}/\Delta V_{SUPPLY})$, in decibels (dB).

TABLE 8. Power Supply Rejection Ratio for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
PSRR	Power Supply Rejection Ratio	2.5V ≤ V+ ≤ 5V	111 105	129		dB
		1.8V ≤ V+ ≤ 5.5V		117		

8

Looking at the top of the datasheet, Figure 12, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, V $_{CM}$ = V+/2, R_L >10k Ω to V+/2. This particular parameter has the V+ conditions in the range of 2.5 to 5, and 1.8 to 5.5 volts.

The PSRR for the LMP7731 at 5V is measured to be 129dB

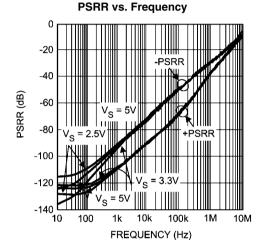


FIGURE 5. Power Supply Rejection Ratio vs. Frequency For LMP7731 (Typical Characteristics)

30070405

A_{VOL} (Open Loop/Large Signal Voltage Gain)

The ratio of the output signal to the input signal (the gain) of the amplifier without external feedback, in decibels (dB).

TABLE 9. Large Signal Voltage Gain for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
A _{VOL}	Large Signal Voltage Gain	$R_L = 10 \text{ k}\Omega \text{ to V+/2}$ $V_O = 0.5 \text{V to 4.5 V}$	112 104	130		dB
		$R_L = 2 \text{ k}\Omega \text{ to V+/2}$ $V_O = 0.5 \text{V to 4.5 V}$	110 94	119		иь

Looking at the top of the datasheet, Figure 14, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, $V_{CM} = V^+/2$, $R_L > 10k\Omega$ to V+/2. This particular parameter has the $R_L = V^+/2$

10k Ω to V+/2 with V $_O$ from 0.5 to 4.5V and R $_L$ = 2k Ω to V+/2 with V $_O$ from 0.5 to 4.5V.

V_O (Voltage Swing)

Table 10. Output Swing High and Low for LMP7731 (Electrical Characteristics)

This is the maximum peak-to-peak voltage swing that can be inputted to the circuit without clipping the signal.

TABLE 10. Offset Voltage for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
V _O	Output Swing High	$R_L = 10 \text{ k}\Omega \text{ to V+/2}$		8	50 75	
		$R_L = 2 \text{ k}\Omega \text{ to V+/2}$		24	50 75	mV from
	Output Swing Low	$R_L = 10 \text{ k}\Omega \text{ to V+/2}$		9	50 75	either rail
		$R_L = 2 \text{ k}\Omega \text{ to V+/2}$		23	50 75	

Looking at the top of the datasheet, Figure 15, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, V_{CM} = V+/2,

 R_L >10k Ω to V+/2. This particular parameter has the R_L = 10k Ω to V+/2 and R_L = 2k Ω to V+/2.

I_O (Output Short Circuit Current)

The amount of current that is drawn from the output.

TABLE 11. Output Short Circuit Current for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
I _O	Output Short Circuit Current	Sourcing, V _O = V+/2	33 27	47		A
		Sinking, $V_0 = V+/2$ V_{IN} (diff) = -100 mV	30 25	49		mA

Looking at the top of the datasheet, Figure 15, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, V_{CM} = V+/2, $R_L > 10 k\Omega$ to V+/2. This particular parameter has the $V_O = V$

+/2 with $\rm V_{IN}$ (diff) = 100mV for sourcing and $\rm V_O$ = V+/2 with $\rm V_{IN}$ (diff) = -100mV for sinking.

I_S (Supply Current)

The current into the Vcc+ and Vcc- inputs required to operate the op amp.

TABLE 12. Supply Current for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
I _S	Supply current (Per Channel)	V _{CM} = 4.5V		2.2	3.0 3.7	mΛ
		V _{CM} = 0.5V		2.5	3.4 4.2	mA

Looking at the top of the datasheet, Figure 15, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V^- = 0V, V_{CM} = V+/2,

 $\rm R_L$ >10k Ω to V+/2. This particular parameter has the $\rm V_{CM}\!\!=\!\!4.5$ and 0.5V.

CMVR (Common Mode Voltage Range)

has exceeded the limits, the op amp will not function as stated in the datasheet.

(Also called Input Voltage Range) The amplifier's common mode voltage operation range. If the common mode voltage

TABLE 13. Input Common-Mode Voltage Range for LMP7731 (Electrical Characteristics)

5V Electrical Characteristics

Unless otherwise specified, all limits are guaranteed for $T_A = 25^{\circ}C$, $V^+ = 5V$, $V^- = 0V$, $V_{CM} = V^+/2$, $R_L > 10 \text{ k}\Omega$ to $V^+/2$. **Boldface** limits apply at the temperature extremes.

Symbol	Parameter	Conditions	Min (Note 6)	Typ (Note 5)	Max (Note 6)	Units
CMVR	Input Common-Mode Voltage Range	Large Signal CMRR ≥ 80 dB	0		5	V

Looking at the top of the datasheet, Figure 12, the LMP7731 is specified at 5V, at 25 °C, V+ = 5V, V- = 0V, $V_{CM} = V^+/2$,

 R_L >10k $\!\Omega$ to V+/2. This particular parameter has the Large Signal CMRR $\! \geq \! 80 \text{dB}.$

Notes

For more National Semiconductor product information and proven design tools, visit the following Web sites at:

Pr	oducts	Design Support		
Amplifiers	www.national.com/amplifiers	WEBENCH	www.national.com/webench	
Audio	www.national.com/audio	Analog University	www.national.com/AU	
Clock Conditioners	www.national.com/timing	App Notes	www.national.com/appnotes	
Data Converters	www.national.com/adc	Distributors	www.national.com/contacts	
Displays	www.national.com/displays	Green Compliance	www.national.com/quality/green	
Ethernet	www.national.com/ethernet	Packaging	www.national.com/packaging	
Interface	www.national.com/interface	Quality and Reliability	www.national.com/quality	
LVDS	www.national.com/lvds	Reference Designs	www.national.com/refdesigns	
Power Management	www.national.com/power	Feedback	www.national.com/feedback	
Switching Regulators	www.national.com/switchers			
LDOs	www.national.com/ldo			
LED Lighting	www.national.com/led			
PowerWise	www.national.com/powerwise			
Serial Digital Interface (SDI)	www.national.com/sdi			
Temperature Sensors	www.national.com/tempsensors			
Wireless (PLL/VCO)	www.national.com/wireless			

THE CONTENTS OF THIS DOCUMENT ARE PROVIDED IN CONNECTION WITH NATIONAL SEMICONDUCTOR CORPORATION ("NATIONAL") PRODUCTS. NATIONAL MAKES NO REPRESENTATIONS OR WARRANTIES WITH RESPECT TO THE ACCURACY OR COMPLETENESS OF THE CONTENTS OF THIS PUBLICATION AND RESERVES THE RIGHT TO MAKE CHANGES TO SPECIFICATIONS AND PRODUCT DESCRIPTIONS AT ANY TIME WITHOUT NOTICE. NO LICENSE, WHETHER EXPRESS, IMPLIED, ARISING BY ESTOPPEL OR OTHERWISE, TO ANY INTELLECTUAL PROPERTY RIGHTS IS GRANTED BY THIS DOCUMENT.

TESTING AND OTHER QUALITY CONTROLS ARE USED TO THE EXTENT NATIONAL DEEMS NECESSARY TO SUPPORT NATIONAL'S PRODUCT WARRANTY. EXCEPT WHERE MANDATED BY GOVERNMENT REQUIREMENTS, TESTING OF ALL PARAMETERS OF EACH PRODUCT IS NOT NECESSARILY PERFORMED. NATIONAL ASSUMES NO LIABILITY FOR APPLICATIONS ASSISTANCE OR BUYER PRODUCT DESIGN. BUYERS ARE RESPONSIBLE FOR THEIR PRODUCTS AND APPLICATIONS USING NATIONAL COMPONENTS. PRIOR TO USING OR DISTRIBUTING ANY PRODUCTS THAT INCLUDE NATIONAL COMPONENTS, BUYERS SHOULD PROVIDE ADEQUATE DESIGN, TESTING AND OPERATING SAFEGUARDS.

EXCEPT AS PROVIDED IN NATIONAL'S TERMS AND CONDITIONS OF SALE FOR SUCH PRODUCTS, NATIONAL ASSUMES NO LIABILITY WHATSOEVER, AND NATIONAL DISCLAIMS ANY EXPRESS OR IMPLIED WARRANTY RELATING TO THE SALE AND/OR USE OF NATIONAL PRODUCTS INCLUDING LIABILITY OR WARRANTIES RELATING TO FITNESS FOR A PARTICULAR PURPOSE, MERCHANTABILITY, OR INFRINGEMENT OF ANY PATENT, COPYRIGHT OR OTHER INTELLECTUAL PROPERTY RIGHT.

LIFE SUPPORT POLICY

NATIONAL'S PRODUCTS ARE NOT AUTHORIZED FOR USE AS CRITICAL COMPONENTS IN LIFE SUPPORT DEVICES OR SYSTEMS WITHOUT THE EXPRESS PRIOR WRITTEN APPROVAL OF THE CHIEF EXECUTIVE OFFICER AND GENERAL COUNSEL OF NATIONAL SEMICONDUCTOR CORPORATION. As used herein:

Life support devices or systems are devices which (a) are intended for surgical implant into the body, or (b) support or sustain life and whose failure to perform when properly used in accordance with instructions for use provided in the labeling can be reasonably expected to result in a significant injury to the user. A critical component is any component in a life support device or system whose failure to perform can be reasonably expected to cause the failure of the life support device or system or to affect its safety or effectiveness.

National Semiconductor and the National Semiconductor logo are registered trademarks of National Semiconductor Corporation. All other brand or product names may be trademarks or registered trademarks of their respective holders.

Copyright© 2008 National Semiconductor Corporation

For the most current product information visit us at www.national.com



National Semiconductor Americas Technical Support Center Email: support@nsc.com Tel: 1-800-272-9959 National Semiconductor Europe Technical Support Center Email: europe.support@nsc.com German Tel: +49 (0) 180 5010 771 English Tel: +44 (0) 870 850 4288 National Semiconductor Asia Pacific Technical Support Center Email: ap.support@nsc.com

National Semiconductor Japan Technical Support Center Email: jpn.feedback@nsc.com